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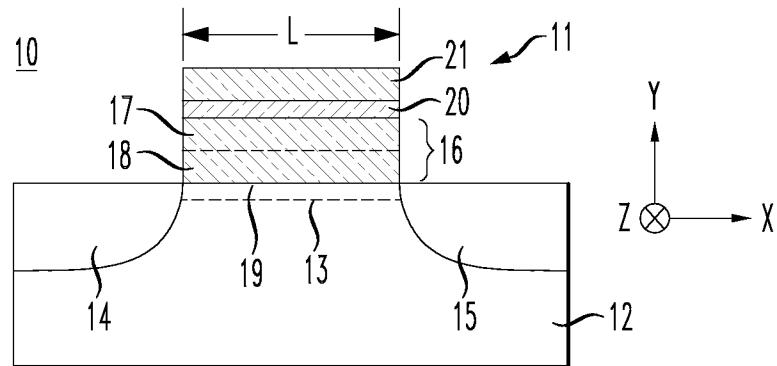


FIG. 1

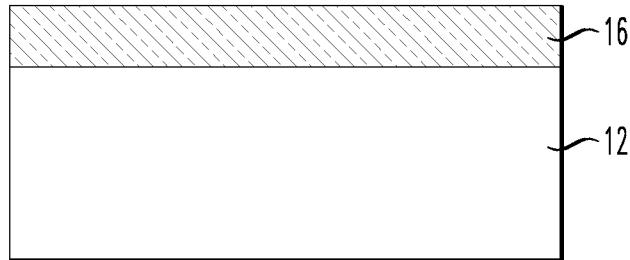


FIG. 2A

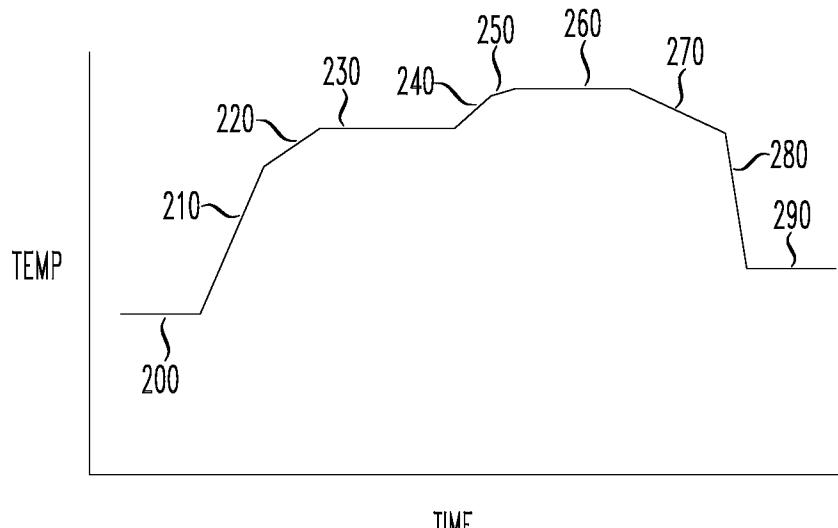


FIG. 2B

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MOS TRANSISTOR AND METHOD OF MANUFACTURE
REPLACEMENT SHEET

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FIG. 3

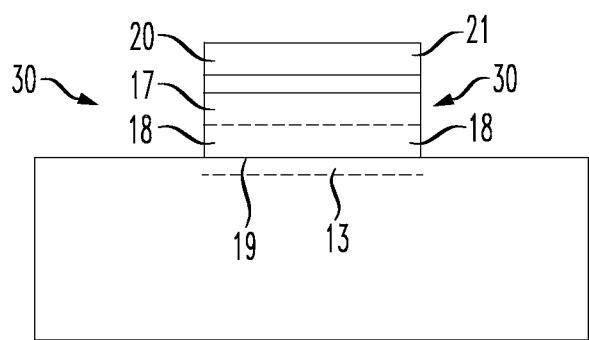


FIG. 4

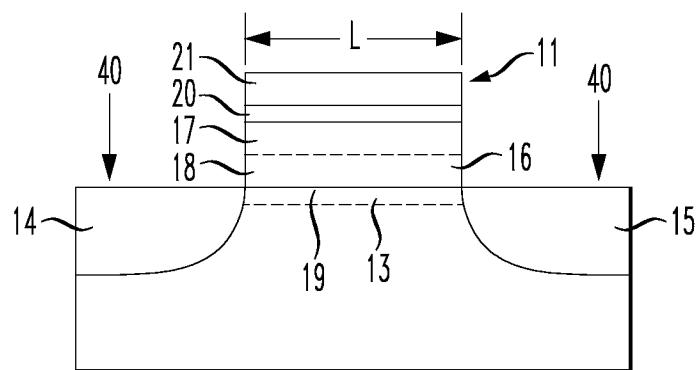


FIG. 5

